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## \*BIBDATASHEET\*

CONFIRMATION NO. 7832

Bib Data Sheet

SERIAL NUMBER 10/630,427	FILING DATE 07/29/2003  RULE	CLASS 438	GROUP ART UNIT 2813	ATTORNEY DOCKET NO. MI22-2378
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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 10/227,500 08/22/2002  
 which is a DIV of 09/848,825 05/03/2001 PAT 6,589,851  
 which is a CON of 09/310,044 05/11/1999 PAT 6,344,399  
 which is a DIV of 08/846,110 04/25/1997 PAT 6,004,835

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 10/23/2003

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY VT	SHEETS DRAWING 12	TOTAL CLAIMS 4	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged  Examiner's Signature	Initials			

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## TITLE

Semiconductor processing methods of forming integrated circuitry, forming conductive lines, forming a conductive grid, forming a conductive network, forming an electrical interconnection to a node location, forming an electrical interconnection with a transistor source/drain region, and integrated circuitry

FILING FEE

FEES: Authority has been given in Paper  
 No. \_\_\_\_\_ to charge/credit DEPOSIT ACCOUNT  
 No. \_\_\_\_\_ for following:

- ☐ All Fees  
☐ 1.16 Fees ( Filing )  
☐ 1.17 Fees ( Processing Ext. of time )

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NO. \_\_\_\_\_ FOR FOLLOWING.

☐ 1.18 Fees ( Issue )

☐ Other \_\_\_\_\_

☐ Credit